


Radio communicator

Patent number: CN1499725
Publication date: 2004-05-26
Inventor: HIDEICHI TAKEDA (JP); MASAO MIYAURA (JP)
Applicant: ALPS ELECTRIC CO LTD (JP)
Classification:
- **International:** H03L7/00
- **European:**
Application number: CN200310115656 20031110
Priority number(s): JP20020324670 20021108

Also published as:

 JP2004159207 (

Report a data error he

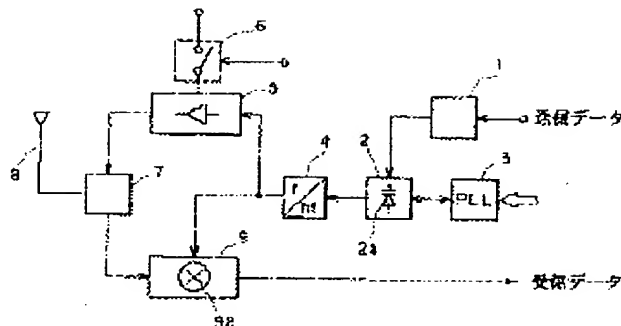
Abstract not available for CN1499725

Abstract of corresponding document: **JP2004159207**

<P>PROBLEM TO BE SOLVED: To prevent an oscillation frequency of a voltage controlled oscillation circuit from being varied even if it is switched from a blind time slot to a transmitting time slot.

<P>SOLUTION: The device comprises a transmitting circuit 5 for transmitting at a predetermined transmitting slot, and a voltage controlled oscillation circuit 2 for conducting direct FM modulation for an oscillation signal with modulated data while its oscillation frequency is controlled by a PLL circuit 3. As for the transmitting circuit 5 during the transmitting slot, a power supply voltage is stopped it during a blind time slot period when the voltage controlled oscillation circuit 2 is locked by the PLL circuit 3, and the power supply voltage is supplied during a transmitting time slot period, following the blind time slot, when the voltage controlled oscillation circuit 2 is lock-released by the PLL circuit 3. In addition, a DC level of modulated data in the transmitting time slot period is set higher or lower than the one in the blind time slot.

<P>COPYRIGHT: (C)2004,JPO



Data supplied from the **esp@cenet** database - Worldwide